

Notice of Allowability	Application No.	Applicant(s)	
	10/771,856	TANAKA, TOSHIMASA	
	Examiner	Art Unit	
	Jimmy H. Nguyen	2629	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to 5/2/07.
2. The allowed claim(s) is/are 1-7 and 18.
3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All
 - b) Some*
 - c) None
 of the:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying Indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)
2. Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date _____
4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. Notice of Informal Patent Application
6. Interview Summary (PTO-413),
Paper No./Mail Date attached hereto .
7. Examiner's Amendment/Comment
8. Examiner's Statement of Reasons for Allowance
9. Other _____.

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Dariush G. Adli on 06/13/2007.

2. The application has been amended as follows:

In the claim:

Claim 1:

Change “**said first**” in line 8 to -- **a third** -- and

Change “**a third**” in line 17 to -- **said third** --.

Claim 3:

Change “**said first output power supply voltage**” in lines 5-6 to -- **a highest output voltage of multiple voltages provided to the display unit** -- and

Insert -- wherein **said first buffer circuit outputting said highest output voltage of said multiple output voltages is energized by said first output power supply voltage**; --immediately after “**voltage;**” in line 11.

Claim 4:

Delete “**said first buffer circuit outputting a highest output voltage of multiple output voltages is energized by said first output power supply voltage;**” in lines 3-4.

Claim 18:

Change “**said first**” in line 15 to -- **a third** -- and

Change “**a third**” in line 25 to -- **said third** --.

REASONS FOR ALLOWANCE

3. The following is an examiner's statement of reasons for allowance: the claimed invention is directed to an electric power unit for driving a display unit in a stable condition with a reduced power and a display unit utilizing such electric power unit. Independent claims 1 and 18 identify at least uniquely distinct features, “a second voltage conversion circuit for stepping down the highest of said group of high output voltages to generate a second output power supply voltage that is lower than the lowest of said group of high output voltages but higher than the highest of said group of low output voltages” in lines 11-15 of claim 1 and in lines 19-23 of claim 18. Independent claim 3 identifies at least uniquely distinct features, “a second voltage conversion circuit for stepping down a highest output voltage of multiple output voltages provided to the display unit; wherein said first buffer circuit outputting said highest output voltage of said multiple output voltages is energized by said first output power supply voltage”; see lines 5-6 of claim 3 and the amendment to claim 3 above. Independent claim 5 identifies at least uniquely distinct features, “a second voltage conversion circuit receiving said first output voltage and stepping down said first output voltage to generate a second output power supply voltage lower than said third output voltage but higher than said fourth output voltage” in lines 20-23. The closest prior arts, Tsuchiya (US 7,061,481 B2), Nanno et al. (US 6,909,413 B2), Yanagi et al.

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(US 7,126,595 B2) all discussed in the Office Action dated 2/2/2007, either singularly or in combination, fail to anticipate or render the above underlined limitations obvious.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jimmy H. Nguyen whose telephone number is 571-272-7675. The examiner can normally be reached on Monday - Thursday, 8:00 a.m. - 5:00 p.m..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin Shalwala can be reached at 571-272-7681. The fax phone number for the organization where this application or proceeding is assigned is (571) 273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



JHN
June 20, 2007

Jimmy H. Nguyen
Primary Examiner
Technology Division: 2629